

REPLACEMENT  
SHEET

FIG.1  
CONVENTIONAL ART

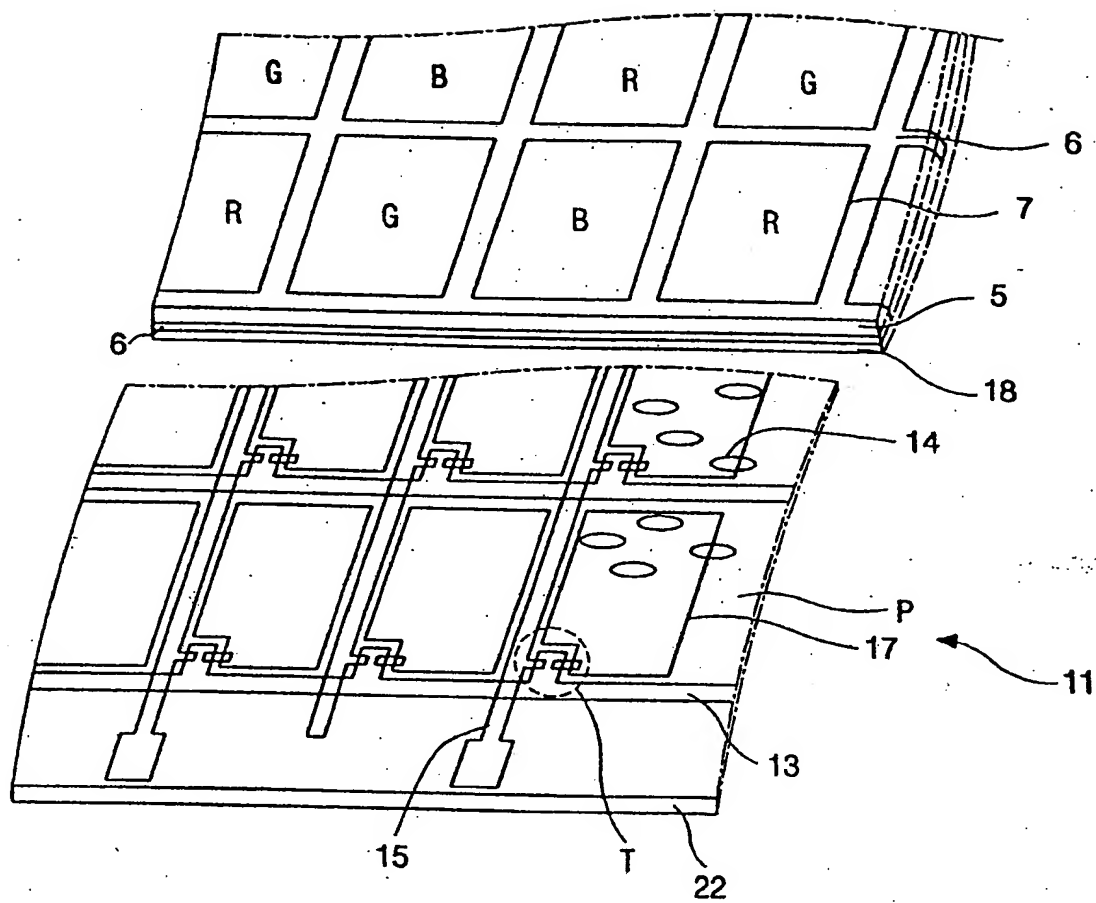
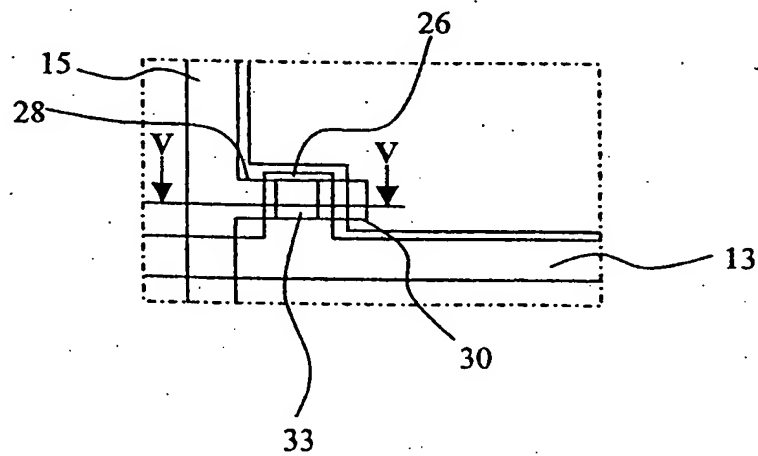
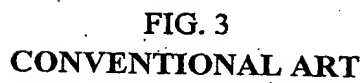


FIG. 2  
CONVENTIONAL ART



# REPLACEMENT SHEET

FIG. 4A  
CONVENTIONAL ART

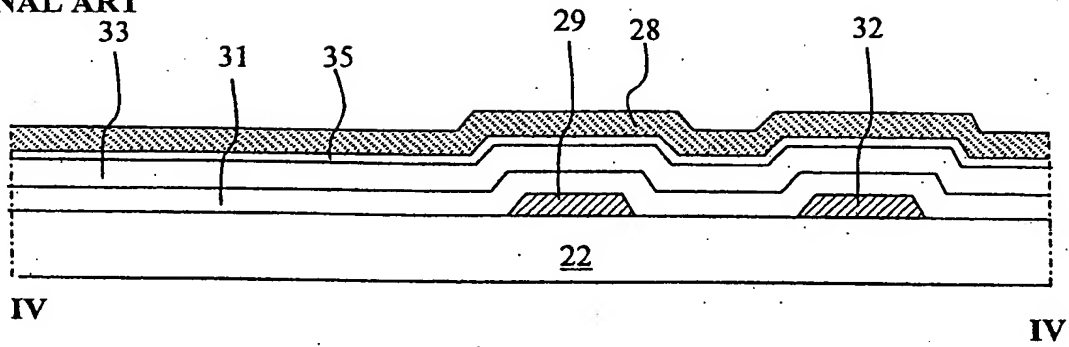


FIG. 4B  
CONVENTIONAL ART

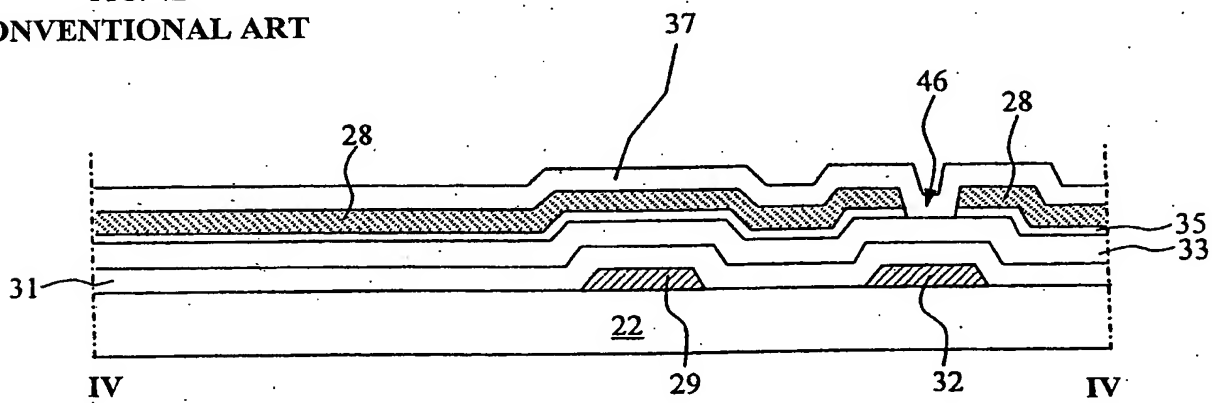
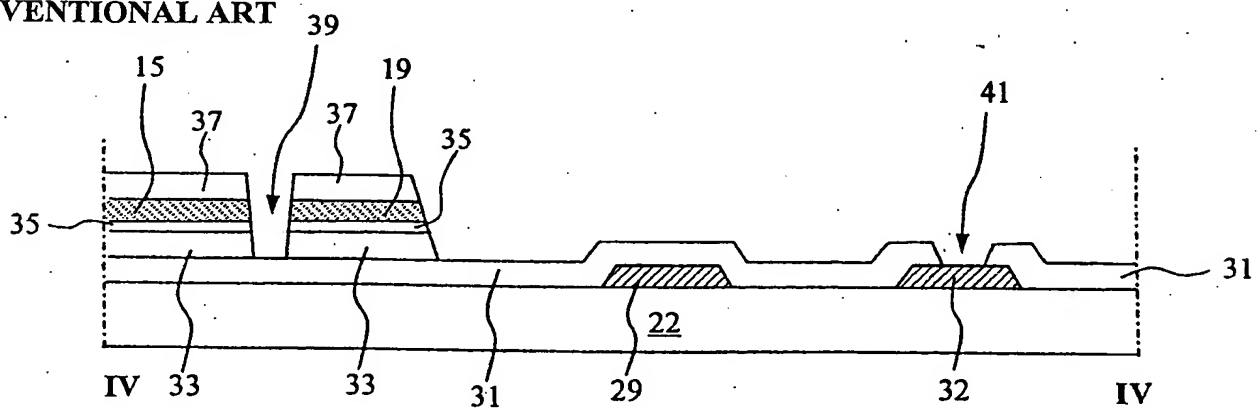


FIG. 4C  
CONVENTIONAL ART



# REPLACEMENT SHEET

FIG. 4D  
CONVENTIONAL ART

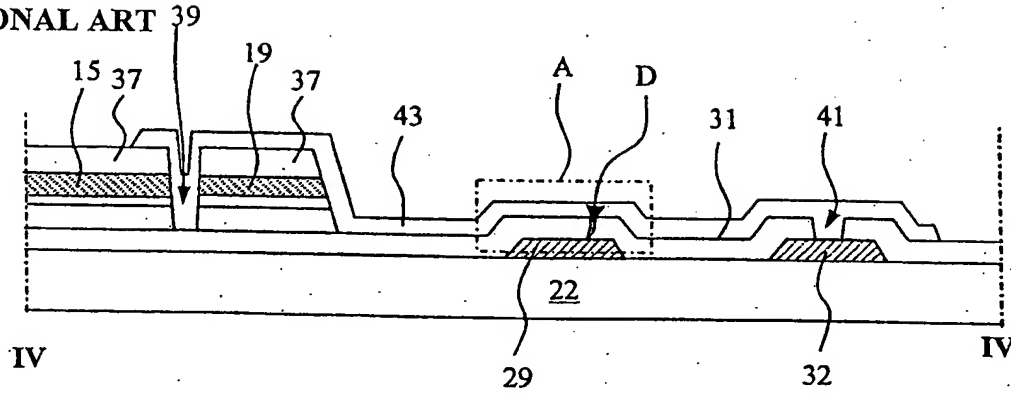


FIG. 5A  
CONVENTIONAL ART

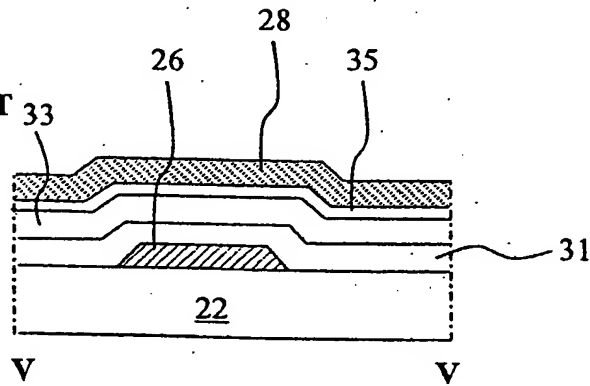


FIG. 5B  
CONVENTIONAL ART

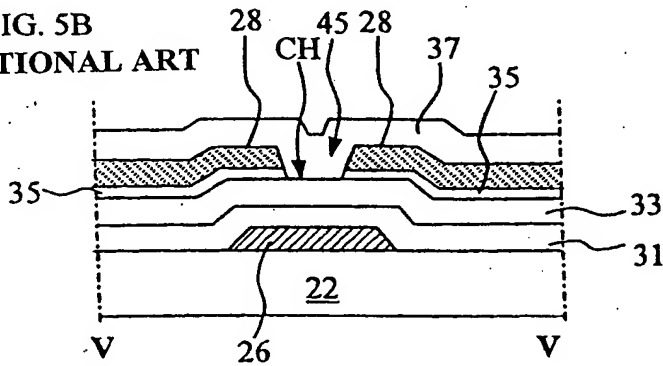


FIG. 5C  
CONVENTIONAL ART

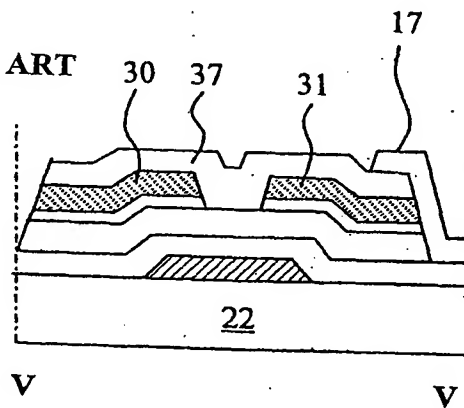


FIG. 6

FIG. 6 is a cross-sectional view of a semiconductor device. The diagram shows a grid of conductive lines. Vertical lines are labeled 140, 141, 151, 140, 151, 141. Horizontal lines are labeled 132, 129, 142, 143, 139, 119. A dashed line is labeled 122. A vertical line is labeled 118. A horizontal line is labeled 117. A vertical line is labeled 113. A horizontal line is labeled 115. A vertical line is labeled VIII. A horizontal line is labeled C. A vertical line is labeled P. A horizontal line is labeled T. The diagram shows a series of vertical and horizontal lines intersecting, with various layers and components labeled accordingly.

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FIG. 8A

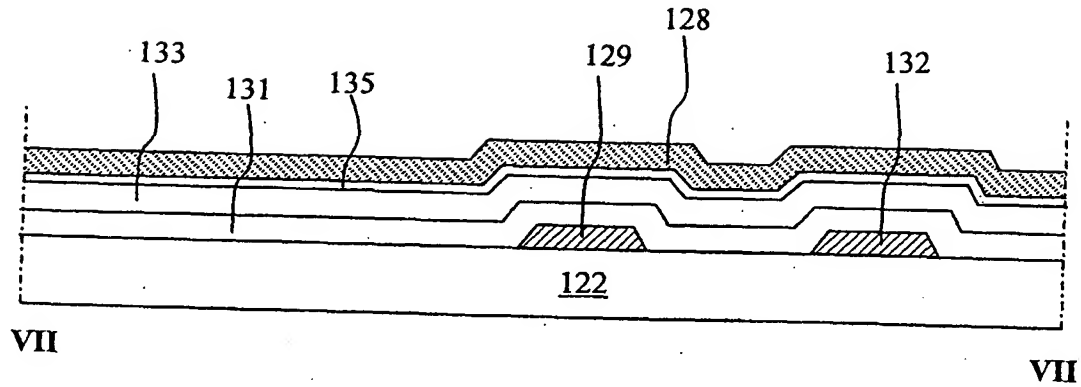


FIG. 8B

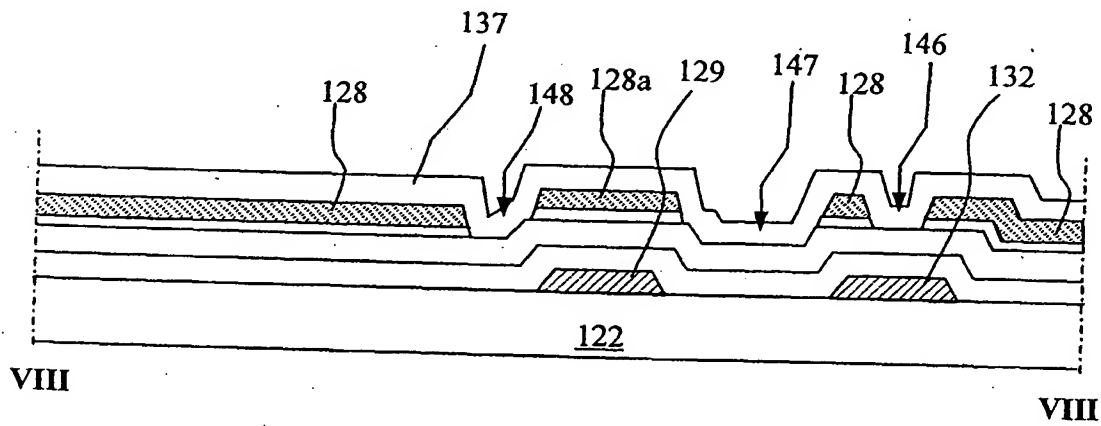
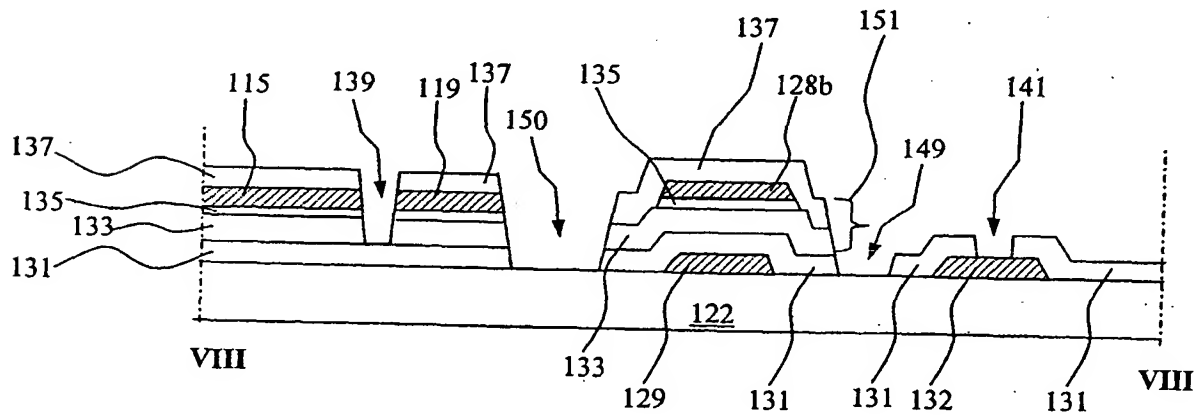


FIG. 8C



# REPLACEMENT SHEET

FIG. 8D

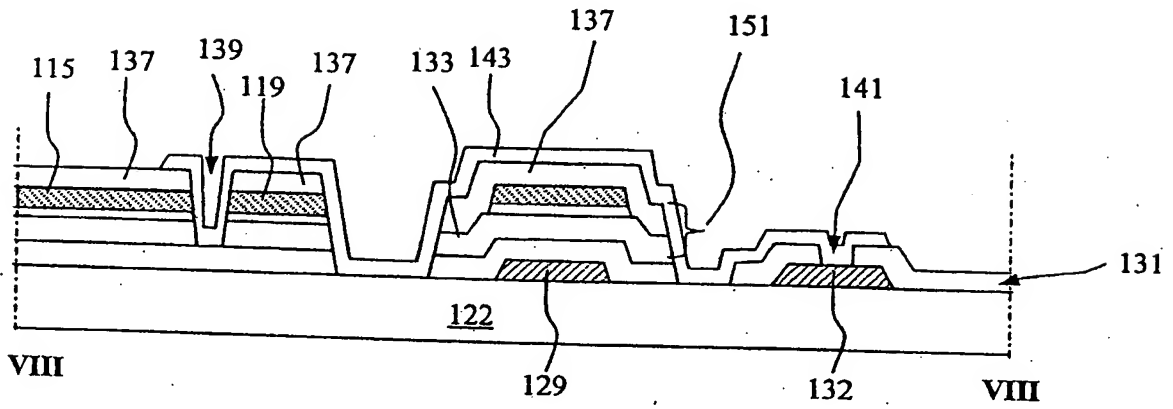


FIG. 9A

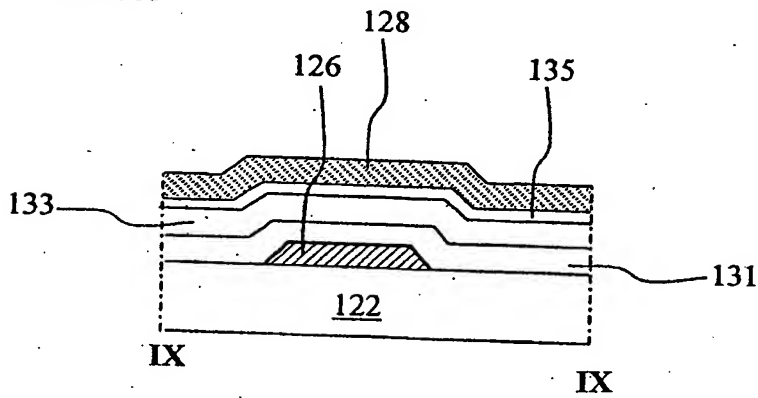
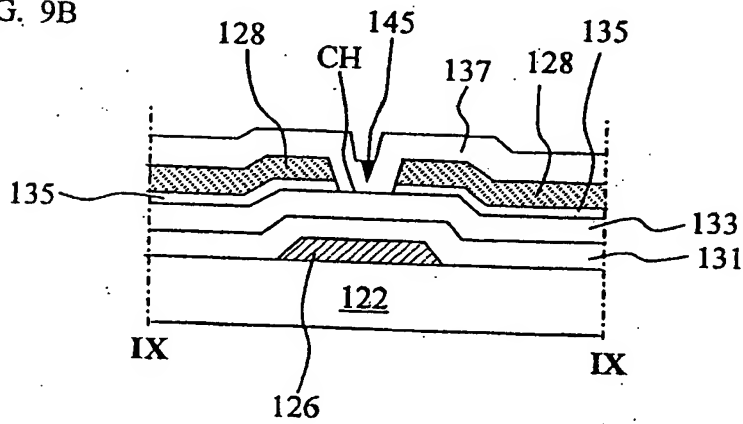


FIG. 9B





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